

# BLAKELY SOKOLOFF TAYLOR & ZAFMAN

A LIMITED LIABILITY PARTNERSHIP INCLUDING LAW CORPORATIONS

TELEPHONE (408) 720-8300

FACSIMILE (408) 720-8383

BSTZ\_MAIL@BSTZ.COM

WWW.BSTZ.COM

INTELLECTUAL PROPERTY LAW

SILICON VALLEY

1279 OAKMEAD PARKWAY  
SUNNYVALE, CALIFORNIA 94085-4040

OTHER OFFICES

LOS ANGELES, CA  
ORANGE COUNTY/COSTA MESA, CA  
DENVER, CO  
PORTLAND/BEAVERTON, OR  
SEATTLE, WA

August 2, 2007

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Re: **CERTIFICATE OF CORRECTION**

U. S. Letters Patent No. 7,007,156

Issued: February 28, 2006

For: MULTIPLE COPROCESSOR ARCHITECTURE TO  
PROCESS A PLURALITY OF SUBTASKS IN  
PARALLEL

Inventor: Stark et al.

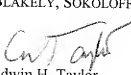
Our File No. 42390.P9926

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.  
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.  
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



Edwin H. Taylor  
Reg. No. 25,129

EHT/sn  
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,007,156  
DATED : February 28, 2006  
INVENTOR(S) : Stark, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 1, at line 42, delete "this" and insert --these--.

MAILING ADDRESS OF SENDER  
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN  
12400 Wilshire Blvd. 7th floor  
Los Angeles, CA 90025-1026

PATENT NO. 7,007,156

**Certificate of Correction (PTO Form 1050)-Amended**